

IP-HFLK40B23(32) CBD

SFP+BI-DI 10Gb/sTX1270nmDFB/RX1330nm (TX1330nmDFB/RX1270nm) Single-mode 40kmDDM

PRODUCT FEATURES

- Up to 11.3Gbps Data Links
- 1270nm DFB laser transmitter and PIN/TIA receiver for IP-HFLK40B23CBD
- 1330nm DFB laser transmitter and PIN/TIA receiver for IP-HFLK40B32CBD
- Up to 40km on 9/125µm SMF
- Hot-pluggable SFP footprint
- BIDILC/UPC type pluggable optical interface
- Low power dissipation
- Metal enclosure, for lower EMI
- ROHS compliant and lead-free
- Support Digital Diagnostic Monitor interface
- Single +3.3V power supply
- Compliant with SFF-8472
- Case operating temperature

Commercial: 0°C to +70°



APPLICATIONS

- 10GBASE-BX & 10GBASE-ER/EW
- 10G SONET/SDH, OTU2/2e

Compliance

- SFF-8472 SFP+ MSA.
- SFP+ SFF-8431 and SFF-8432.
- IEEE802.3ae
- ROHS

PRODUCT DESCRIPTION

IP-HFLK40B23(32) CBD SFP+ transceivers are compatible with the Small Form Factor Pluggable Multi-Sourcing Agreement (MSA). The transceiver consists of five sections: the LD driver, the limiting amplifier, the digital diagnostic monitor, the 1270nm DFB laser (the 1330nm DFB laser) and the PIN/TIA. The module data link up to 40km in 9/125um Single-mode fiber.

This transceiver meets SFP+ industry standard package utilizing an integral LC-Bi-directional optical interface connector. An enhanced Digital Diagnostic Monitoring Interface compliant with SFF-8472 has been incorporated into the transceiver. It allows real time access to the transceiver operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage by reading a built-in memory with I²C interface.

The optical output can be disabled by a LVTTTL logic high-level input of Tx Disable, and the system also can disable the module via I²C. Tx Fault is provided to indicate that degradation of the laser. Loss of signal (LOS) output is provided to indicate the loss of an input optical signal of receiver or the link status with partner. The system can also get the LOS (or Link)/Disable/Fault information via I²C register access.

I. Contact Information

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Revision History

Version No.	Date	Description
1.0	June 24, 2019	Preliminary datasheet